CL-Q28-LR4-20
100G QSFP28 Optical Transceiver Module
RoHS 6 compliant



Features

- 4 Independent LAN-WDM channels
- Low Power Consumption <4W
- Wide Operating Temperature(0°C~70°C)
- Maximum link length of 20km via Single Mode Fiber (SMF)

Applications

- 100GBASE LR4 100G Ethernet
- High performance computing, data com and sever data links
- High speed access

PART NUMBER	Monitor	INPUT/OUTPUT	SIGNAL DETECT	TEMPERATURE
CL-Q28-LR4-20	X	AC/AC	TTL	0°C to 70 °C

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I. Absolute Maximum Ratings

Parameter	Symbol	Conditions	Min.	Max.	Unit
Storage Temperature	$T_{Storage}$		-40	+85	°C
Relative Humidity	RH		0	+85	%

II. Recommended Operating Conditions

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Case Temperature	T_C		0		70	°C
Power Supply Voltage	V_{CC}		3.135	3.3	3.465	V
Signaling Rate each Channel				25.78125		Gbps
Supply Noise Rejection			---	---	100	mV
Receiver Differential Data Output			---	100		Ohm
Operating Distance	D		---	---	20	km

III. Electrical Characteristics

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Power Consumption					4	W
Supply Current	Icc				1250	mA

IV. Optical Characteristics(Ta, unless noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Transmitter						
Signaling rate, each lane (range)	GBb			25.78125		GBb
Center Wavelength	λ0		1294.53		1296.59	nm
	λ1		1299.02		1301.09	nm

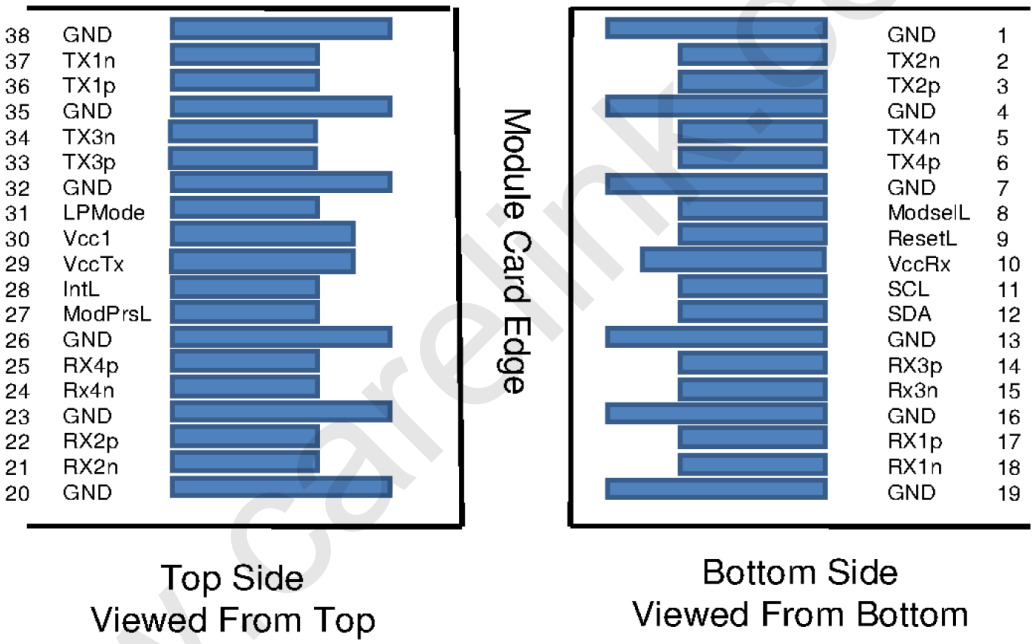
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	λ2		1303.54		1305.63	nm
	λ3		1308.09		1310.19	nm
Side-mode suppression ratio	SMSR		30			dB
Total average launch power					10.5	dBm
Average launch power, each lane	Pf		-2.3		4.5	dBm
Optical Modulation Amplitude (OMA), each lane	TxOMA		-1.3		4.5	dBm
Transmitter and Dispersion Penalty	TDP				2.2	dB
Average launch power of OFF transmitter, each lane					-30	dBm
Extinction ratio	ER		4			dB
Optical return loss tolerance					20	dB
Transmitter reflectance					-12	dB
Receiver						
Signaling rate, each lane (range)	GBb			25.78125		GBb
Center Wavelength	λ0		1294.53		1296.59	nm
	λ1		1299.02		1301.09	nm
	λ2		1303.54		1305.63	nm
	λ3		1308.09		1310.19	nm
Damage threshold			5.5			dBm
Average power at receiver input, each lane			-10.6		4.5	dBm
Receive power, each lane (OMA)					4.5	dBm
Return Loss	RL		-26			dB

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Receiver sensitivity (OMA)	S_{OMA}	BER@10e-12			-9.3	dBm
LOS Assert	LOS_A		-24			dBm
LOS De-Assert	LOS_D				-10.8	dBm
LOS Hysteresis			0.5			dB

V. PIN Assignmen



VI. Pin Definitions

PIN	Logic	Symbol	Name/Description	Note
1		GND	Ground	
2	CML-I	Tx2n	Transmitter Inverted Data Input	
3	CML-I	Tx2p	Transmitter Non-Inverted Data output	
4		GND	Ground	
5	CML-I	Tx4n	Transmitter Inverted Data Input	

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6	CML-I	Tx4p	Transmitter Non-Inverted Data output	
7		GND	Ground	
8	LVTTL-I	ModSelL	Module Select	
9	LVTTL-I	ResetL	Module Reset	
10		VccRx	+3.3V Power Supply Receiver	
11	LVCMOS-I/O	SCL	2-Wire Serial Interface Clock	
12	LVCMOS-I/O	SDA	2-Wire Serial Interface Data	
13		GND	Ground	
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	
15	CML-O	Rx3n	Receiver Inverted Data Output	
16		GND	Ground	
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	
18	CML-O	Rx1n	Receiver Inverted Data Output	
19		GND	Ground	
20		GND	Ground	
21	CML-O	Rx2n	Receiver Inverted Data Output	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	
23		GND	Ground	
24	CML-O	Rx4n	Receiver Inverted Data Output	
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	
26		GND	Ground	
27	LVTTL-O	ModPrsL	Module Present	
28	LVTTL-O	IntL	Interrupt	
29		VccTx	+3.3 V Power Supply transmitter	
30		Vcc1	+3.3 V Power Supply	

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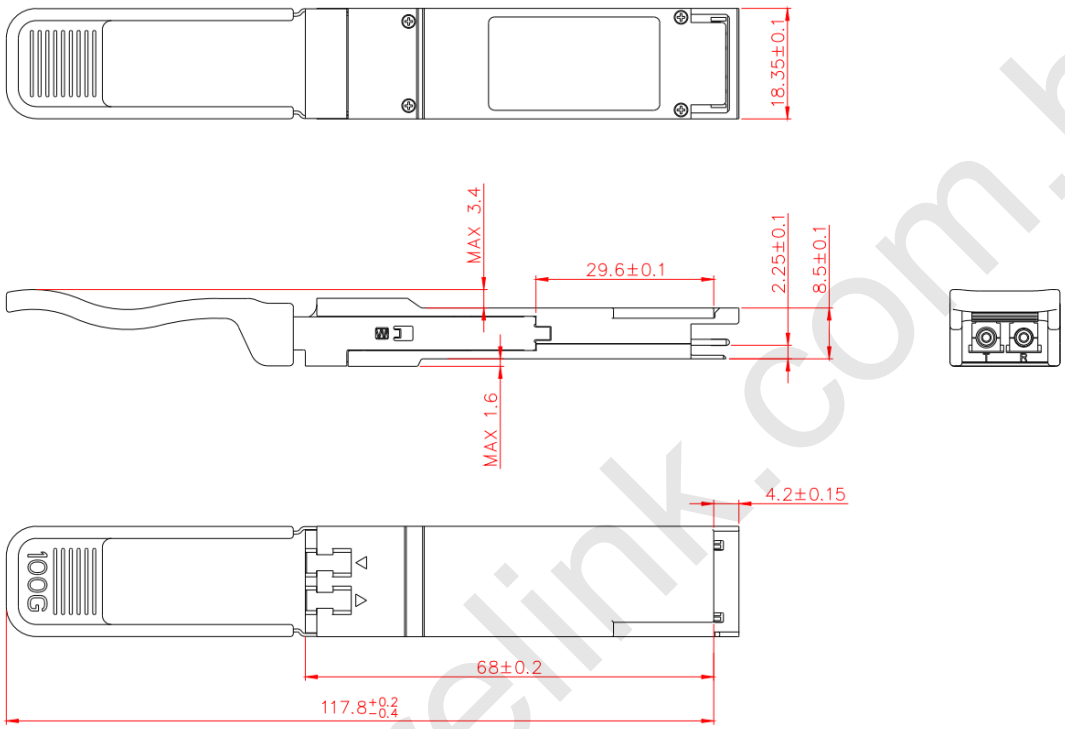
31	LVTTL-I	LPMode	Low Power Mode	
32		GND	Ground	
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input	
34	CML-I	Tx3n	Transmitter Inverted Data Output	
35		GND	Ground	
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	
37	CML-I	Tx1n	Transmitter Inverted Data Output	
38		GND	Ground	

VII. Digital Diagnostic Monitor Accuracy

Parameter	Accuracy	Unit
Internally measured transceiver temperature	+/-3	deg.C
Internally measured transceiver supply voltage	+/-3	%
Measured Tx bias current	+/-10	%
Measured Tx output power	+/-3	dB
Measured Rx received average optical power	+/-3	dB

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VIII. Mechanical Diagram



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